

**31<sup>st</sup> January 2012**

**QUARTERLY ACTIVITIES REPORT FOR THE QUARTER  
ENDED 31<sup>st</sup> December 2011**

The Directors of K2 Energy Limited (ASX: KTE) provide the following report for the Quarter ended 31<sup>st</sup> December 2011.

**MEARS SOLAR**

During the quarter a reviewed design has been developed, and the process of fabrication commenced. The new horizontal design is intended to overcome the recombination issues encountered in the earlier version.

Development work on the new design is on schedule, with fabrication currently taking place at the CNS facility at Harvard. Testing of the wafers produced during the design is expected during the March 2012 quarter.

The MST™ technology involves the production of an altered or “nano-doped” layer of silicon that can be used in the manufacturing of silicon solar PV cells; a process already proven in traditional semiconductor manufacturing. This layer is introduced to increase the efficiency parameter for Photovoltaic (PV) technology and solar power generation in silicon PV cells. By improving the efficiency parameter, the use of MST™ technology in the PV cell development process reduces the amount of silicon required, potentially making PV cells significantly more efficient and much less expensive.

In 2010, K2 Energy Limited, located in Sydney, Australia, acquired the exclusive rights to the MST™ IP for all solar applications from Mears Technologies. K2 has provided the funding necessary for the R&D required to test and develop Mears’ MST™ technology for use by large-scale solar panel manufacturers.

**MEARS TECHNOLOGIES INC.**

K2 Energy Limited’s interest in Mears Technologies Inc. is currently 8% with warrants which, if exercised, would increase K2’s interest to 15% of Mears Technologies Inc.

Mears Technologies Inc. has developed MST™ CMOS technology for use by semiconductor chip producers. The technology addresses key issues that are being encountered by the semiconductor industry.

MST™ has been demonstrated to reduce gate leakage and increase drive current (performance) in CMOS semiconductors. It also has the benefit of reducing the increasing variability in key parameters, that is now one of the most significant problems facing the industry and which is limiting the yield, power and performance of leading products.

The company's core competency combines materials engineering and quantum mechanics with semiconductor process technology, to optimise the power efficiency and performance of integrated circuits manufactured on deep sub-micron processes. With a licensing model and strong patent position covering new silicon structures, methods and processes, Mears Technologies enhances the fundamental electronic properties of silicon, without requiring new manufacturing equipment or the use of exotic materials.

## **OIL AND GAS**

KTE retains its 30% shareholding in Trey Resources I LLC, a US-based limited liability company. During the quarter there was no expenditure incurred in relation to this investment.